

# YOU ARE NEXT GEN OF EXCELLENCE

## OPEN POSITIONS:

- 4 Analog Custom IC Layout Engineer
- 3 Analog Mixed-Signal Design and Verification Engineer
- 10 Digital Design Verification Engineer
- 2 Hardware Design Engineer
- 3 Digital IC Design Engineer
- 1 Analog Design Engineer

## IF YOU ARE A STUDENT OF ONE OF THE LISTED FACULTIES:

School Of Electrical Engineering, University In Belgrade  
Faculty Of Technical Sciences, University In Novi Sad  
Faculty Of Electronic Engineering, University In Niš  
Faculty of Electrical Engineering, University of Banja Luka  
or other relevant educational institutions, feel free to apply

## DEADLINE FOR REGISTRATION:

All received application sent before **19th June** will be taken into consideration and candidates notified about their application status in a timely manner.  
**Internship program is planned to start on 18th July.**



Find out more on  
[elsys-eastern.com/career/internships/](https://elsys-eastern.com/career/internships/)

**APPLY NOW!**